

Update October 11, 2017

**AOS/ES1**

	Ora e aula	AOS Ore	ES1 Less Ex	ES1 Less Ex	Topic	Speaker	Legenda
Monday, 18 September 2017							
Tuesday, 19 September 2017	15.15-18.15 L26.02	3	3	3	Courses presentation	WF	Shared
Wednesday, 20 September 2017							AOS
Thursday, 21 September 2017	12.15-15.15 L26.01	3	3	3	Introduction to Embedded Systems - p1	WF	ES
Friday, 22 September 2017	10.15-12.15 D04	2	2	2	Introduction to Embedded Systems - p2	WF	
Saturday, 23 September 2017							
Sunday, 24 September 2017							
Monday, 25 September 2017							
Tuesday, 26 September 2017	15.15-18.15 L26.02	3			Annullata	WF	
Wednesday, 27 September 2017							
Thursday, 28 September 2017	12.15-15.15 L26.01	3	3	3	Linux Application Development Toolkit	GM	
Friday, 29 September 2017	10.15-12.15 D04	2			Annullata		
Saturday, 30 September 2017							
Sunday, 1 October 2017							
Monday, 2 October 2017							
Tuesday, 3 October 2017	15.15-18.15 L26.02	3	3	3	Code Versioning with GIT	GM	
Wednesday, 4 October 2017							
Thursday, 5 October 2017	12.15-15.15 L26.01	3	3	3	Moving to C++	GM	
Friday, 6 October 2017	10.15-12.15 D04	2	2		Scheduling CPU for General Purpose Systems	WF	
Saturday, 7 October 2017							
Sunday, 8 October 2017							
Monday, 9 October 2017							
Tuesday, 10 October 2017	15.15-18.15 L26.02	3		3	ES: PC-based design of ES	WF	
Wednesday, 11 October 2017							
Thursday, 12 October 2017	12.15-15.15 L26.01	3	3		Multithread Programming in C++ - p1	GM/FT	
Friday, 13 October 2017	10.15-12.15 D04	2	2		Multithread Programming in C++ - p2	GM/FT	
Saturday, 14 October 2017							
Sunday, 15 October 2017							
Monday, 16 October 2017							
Tuesday, 17 October 2017	15.15-18.15 L26.02	3		3	ES	WF	
Wednesday, 18 October 2017							
Thursday, 19 October 2017	12.15-15.15 L26.01	3	3		Multiprocess Programming in C	GM	
Friday, 20 October 2017	10.15-12.15 D04	2	2	2	Scheduling for Real - Time	WF	
Saturday, 21 October 2017							
Sunday, 22 October 2017							
Monday, 23 October 2017							
Tuesday, 24 October 2017	15.15-18.15 L26.02	3		3	WIP: Intro NoC+Bus eser	DZ	
Wednesday, 25 October 2017							
Thursday, 26 October 2017	12.15-15.15 L26.01	3	3		Introduction to Real-Time OS	WF	
Friday, 27 October 2017	10.15-12.15 D04	2	2	2	Project presentation 1		
Saturday, 28 October 2017							
Sunday, 29 October 2017							

Monday, 30 October 2017								
Tuesday, 31 October 2017	15.15-18.15	L26.02	3		3	ES		???
Wednesday, 1 November 2017								
Thursday, 2 November 2017	12.15-15.15	L26.01	3		3	Embedded from scratch: Microcontrollers Boot		FT
Friday, 3 November 2017	10.15-12.15	D04	2		2	Embedded from scratch: Peripherals and Interrupts		FT
Saturday, 4 November 2017								
Sunday, 5 November 2017								
Monday, 6 November 2017								
Tuesday, 7 November 2017	15.15-18.15	L26.02	3		3	WIP: cycle accurate simulators + gem5 internals		DZ
Wednesday, 8 November 2017								
Thursday, 9 November 2017	12.15-15.15	L26.01	3		3	Miosix: Boot with Operating System		FT
Friday, 10 November 2017	10.15-12.15	D04	2		2	WIP: gem5 DSE (2 ore)		DZ
Saturday, 11 November 2017								
Sunday, 12 November 2017								
Monday, 13 November 2017								
Tuesday, 14 November 2017	15.15-18.15	L26.02	3		3	no lesson?		WF
Wednesday, 15 November 2017	13.15-15.15	D1.1	2					
Thursday, 16 November 2017	12.15-15.15	L26.01	3		3	no lesson?		DZ
Friday, 17 November 2017	10.15-12.15	D04	2					
Saturday, 18 November 2017								
Sunday, 19 November 2017								
Monday, 20 November 2017								
Tuesday, 21 November 2017	15.15-18.15	L26.02	3		3	ES		WF
Wednesday, 22 November 2017	13.15-15.15	D1.1	2		2	Miosix: Context switching, Time management		FT
Thursday, 23 November 2017	12.15-15.15	L26.01	3		3	SystemVerilog: Verification		DZ
Friday, 24 November 2017	10.15-12.15	D04	2					
Saturday, 25 November 2017								
Sunday, 26 November 2017								
Monday, 27 November 2017								
Tuesday, 28 November 2017	15.15-18.15	L26.02	3		3	AOS		WF
Wednesday, 29 November 2017	13.15-15.15	D1.1	2		2	Miosix: System calls and Filesystem		FT
Thursday, 30 November 2017	12.15-15.15	L26.01	3		3	SystemVerilog: Vivado + Esercizi Base		DZ
Friday, 1 December 2017	10.15-12.15	D04	2					
Saturday, 2 December 2017								
Sunday, 3 December 2017								
Monday, 4 December 2017								
Tuesday, 5 December 2017	15.15-18.15	L26.02	3		3	SystemVerilog: Esercizi FSM + da temi d'esame		DZ
Wednesday, 6 December 2017	13.15-15.15	D1.1	2					
Thursday, 7 December 2017	vacanza							
Friday, 8 December 2017	vacanza							
Saturday, 9 December 2017								
Sunday, 10 December 2017								
Monday, 11 December 2017								
Tuesday, 12 December 2017	15.15-18.15	L26.02	3					
Wednesday, 13 December 2017	13.15-15.15	D1.1	2		2	Project presentation 2 (2 hours)		DZ
Thursday, 14 December 2017	12.15-15.15	L26.01	3					

Friday, 15 December 2017	10.15-12.15 D04	2
Saturday, 16 December 2017		
Sunday, 17 December 2017		
Monday, 18 December 2017		
Tuesday, 19 December 2017	15.15-18.15 L26.02	3
Wednesday, 20 December 2017	13.15-15.15 D1.1	2
Thursday, 21 December 2017	12.15-15.15 L26.01	3
Friday, 22 December 2017	10.15-12.15 D04	2
Saturday, 23 December 2017		
Sunday, 24 December 2017		



