

# CURRICULUM VITÆ

MARCO D. SANTAMBROGIO

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# 1 PERSONAL DATA

## 1.1 General data

**Name** Marco Domenico

**Surname** Santambrogio

**Date of birth** November 4, 1977

**Place of birth** Monza (MB) - Italy

**Citizenship** Italian

**Marital status** Married

**Office @ PoliMi** Dipartimento di Elettronica e Informazione  
Politecnico di Milano  
Via G. Ponzio 34/5  
Milano, Italy 20133

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Office phone: +39 02 2399 4012  
Office Fax: +39 02 2399 3411

## 1.2 Publications

- SCOPUS Author ID: 11540913800 - <https://www.scopus.com/authid/detail.uri?authorId=11540913800> [ h-index: 20 @ April 2017]
- ORCID: 0000-0002-9883-9693 - <https://orcid.org/0000-0002-9883-9693>
- Google Scholar: <https://scholar.google.com/citations?user=Dr2SsKIAAAAJ> [h-index: 29, i10-index: 69 @ April 2017]
- DBLP: [http://dblp.uni-trier.de/pers/hd/s/Santambrogio:Marco\\_D=.html](http://dblp.uni-trier.de/pers/hd/s/Santambrogio:Marco_D=.html)
- Research Gate: [https://www.researchgate.net/profile/Marco\\_Santambrogio](https://www.researchgate.net/profile/Marco_Santambrogio)

## 1.3 Academic positions and affiliations

- Academic positions

**Since 2/2011** Assistant Professor (with tenure) at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).

**Since 12/2009** Adjunct Professor in the College of Engineering of University of Illinois at Chicago (Chicago, Illinois, USA).

**03/2010 - 03/2015** Research Affiliate at Massachusetts Institute of Technology - Computer Science and Artificial Intelligence Laboratory (Cambridge, MA - USA).

**11/2009 - 1/2011** Postdoc/Research Assistant at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).

**02/2009 - 02/2010** Postdoc Fellow (research : Self-Aware Computing Systems.) at Massachusetts Institute of Technology - Computer Science and Artificial Intelligence Laboratory (Cambridge, MA - USA).

**07/2008 - 10/2009** Research Assistant (research program: Metodologie di progetto di sistemi informatici hardware e software. (Design methodologies for hardware and software IT systems.)) at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).

- 03/2008-07/2008** Research Assistant (research program: Analisi e definizione di possibili scenari applicativi di nuovi sistemi riconfigurabili. (Analysis and definition of applicative scenarios for novel reconfigurable technologies.)) at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).
- 03/2005-02/2008** PhD Student in the Computer Engineering and Automation program at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).
- 11/2004-03/2005** Research Assistant (research program: Definizione di un flusso di design per sistemi riconfigurabili basati su FPGA della Xilinx. (Definition of a novel design flow for Xilinx FPGA-based reconfigurable systems.)) at the Dipartimento di Elettronica ed Informazione of Politecnico di Milano (Milano, Italy).

- Affiliations

- Since 2013** Senior Member of the Association for Computing Machinery (ACM)
- Since 2011** Senior Member of the Institute of Electrical and Electronic Engineers (IEEE)
- Since 2009** Member of the Italian Scientists and Scholars of North America Foundation (ISSNAF)
- Since 2008** Member of HiPEAC Reconfigurable Computing Cluster
- Since 2008** Member of HipEAC, European Network of Excellence on High Performance and Embedded Architecture and Compilation
- Since 2008** Member of the Association for Computing Machinery (ACM)
- Since 2008** Member of the IEEE Circuits and Systems Society (CAS)
- Since 2008** Member of the IEEE Computer Society (CS)
- Since 2005** Member of the Institute of Electrical and Electronic Engineers (IEEE)

## 1.4 Education

- 15 May, 2008 PhD in Information Engineering**  
 DIPARTIMENTO DI ELETTRONICA ED INFORMAZIONE, POLITECNICO DI MILANO  
 PhD Thesis: *Hardware/Software codesign methodologies for dynamically reconfigurable systems.*  
 Advisor: Prof. D. Sciuto. Tutor: Prof. F. Ferrandi
- June 2004 Master of Science in Computer Science**  
 UNIVERSITY OF ILLINOIS AT CHICAGO, CHICAGO, ILLINOIS, USA  
 Master Thesis: *Dynamic Reconfigurability in Embedded System Design. A Model for the Dynamic Reconfiguration.*  
 Advisor: Prof. John Lillis.
- April 2004 Laurea (MSc equivalent) in Computer Engineering**  
 POLITECNICO DI MILANO  
 Thesis: *A Methodology for Dynamic Reconfigurability in Embedded System Design.*  
 Advisor: Prof. D. Sciuto.

## 1.5 Visiting periods

- Postdoc Fellow, Massachusetts Institute of Technology, February 2009 - February 2010
- Visiting researcher, Heinz Nixdorf Institute, January 2009
- Invited researcher, Northwestern University, April - May 2007
- Invited researcher, Northwestern University, February - June 2006
- Visiting researcher, Heinz Nixdorf Institute, January 2006
- MSc Student, University of Illinois at Chicago, 2002

## 1.6 Service Activities

- @ Politecnico di Milano

**Since 2017** Member of the organizing committee of the *Innovation in Teaching Activities Initiative* for DEIB

**Since 2013** Member of the organizing committee of the OpenHouse event (Information Technology Section)

**Since 2010** Organizer of the OpenDay event on behalf of the DEIB Information Technology Section

**2008 - 2012** Founder and head of the PhDEI *Association*.

**Since 2011** Founder and head of the Rocca Fellows community

**07/2005 - 12/2006** Head of the ICT area and member of the managing board of ISF-MI Association.

**July 2005** Co-founder of Engineer Without Border Milano (Ingegneria Senza Frontiere ISF-MI).

- @ Others

**Since 2016** Member of the IEEE CS Member & Geographic Activities Board

**Since 2016** Chair of the IEEE CS ARC Larson Paper Prize

**Since Oct 2015** Vice-Chair of the IEEE Italian Chapter of the Computer Society

**Since 2013** Member of the IEEE Italy Section as Student Activities Coordinator.

**2012 - 2015** Member of the IEEE CS Student Awards Committee (SAC).

**Since 2012** Member of the IEEE CS Awards and Recognition Committee (ARC) - <https://www.computer.org/web/chapters/mga-board>

**2009 - 2012** Member of the MIT Postdoctoral Advisory Council.

**2009 - 2012** Member of the National Postdoctoral Association (NAP).

**01/2009** Co-founder and president of the iDRESA Association.

**01/2001- 04/2001** Head of the IT area, for Caritas Ambrosiana and CeLIM, of two humanitarian missions in Kosovo (Jacova e Pristina)

**02/2000 - 12/2000** Civil service at CeLIM NGO.

## 2 SCIENTIFIC ACTIVITIES

### 2.0.1 International Conferences and Journal Organization

- **Editorial advisory board members:**

- Title: Elsevier Editorial Board of Big Data Research, Editor of the area Cyber-Physical Systems  
Editor: Elsevier  
References: <http://www.journals.elsevier.com/big-data-research/editorial-board/> From: Dec 17, 2013

- **Journal Associate Editor**

- ACM Transactions on Reconfigurable Technology and Systems - Special section on the 22nd Reconfigurable Architectures Workshop (RAW 2014)
- ACM Transactions on Reconfigurable Technology and Systems - Special section on the 10th International Symposium on Reconfigurable Computing: Architectures, Tools, and Applications (ARC 2014)
- Elsevier Journal on Future Generation Computer Systems (FGCS) - Special issue on the 10th International Conference on Embedded and Ubiquitous Computing (EUC 2012) (special session title: Multicore and Many-core Architectures for Future Generation Embedded)

- **Journal Guest Editor**

- Microprocessor and Microsystems Journal, Special Issue  
Title: Embedded Multicore Systems: Architecture, Performance and Application.  
Editor: Elsevier  
Guest editors: M. Chang, P.A. Hsiung, M. D. Santambrogio, W. H. Lee  
References: <http://www.elsevier.com/locate/jm/journalInfo>
- EURASIP Journal of Reconfigurable Computing  
Title: Self-Awareness in Reconfigurable Computing Systems.  
Editor: Hindawi Publishing Corporation  
Guest editors: T. Becker, M. D. Santambrogio, M. Happe, M. Platzner,  
References: <http://www.hindawi.com/journals/ijrc/si/807910/cfp/>
- International Journal on Reconfigurable Computing, Special Issue  
Title: Selected Papers from the International Conference on Reconfigurable Computing and FPGAs (ReConFig'10)  
Editor: Hindawi Publishing Corporation
- Journal of Systems Architecture  
Title: Design Flows and System Architectures for Adaptive Computing on Reconfigurable Platforms.  
Editor: Elsevier  
Guest editors: M. D. Santambrogio, I. Bravo  
References: <http://www.elsevier.com/inca/publications/misc/JSASI-AdaptiveComputingTrends.pdf>
- EURASIP Journal on Embedded Systems  
Title: Reconfigurable computing and hardware/software codesign.  
Editor: Hindawi Publishing Corporation  
Guest editors: T. Plaks, M. D. Santambrogio, D. Sciuto  
References: [www.hindawi.com/journals/es/si/rcc.pdf](http://www.hindawi.com/journals/es/si/rcc.pdf)  
[www.hindawi.com/journals/es/raa.731830.html](http://www.hindawi.com/journals/es/raa.731830.html)

### 2.0.2 Steering Committee

- IEEE International Conference on Cyber, Physical and Social Computing (CPSCom): 2017
- Embedded Operating System Workshop (EWiLi): 2016, 2017
- IEEE International Conference on Networking, Architecture, and Storage (NAS): 2014, 2015

### **2.0.3 General Chair**

- Annual IEEE International Conference on Application-specific Systems, Architectures and Processors (ASAP): 2017
- IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC): 2014
- IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA): 2014
- IEEE International Conference on Cyber, Physical and Social Computing (CPSCoM): 2013

### **2.0.4 Program Chair**

- IEEE International Conference on Field Programmable Logic and Applications (FPL): 2010 (226 submitted papers, 60 regular papers), 2017
- IEEE International Conference on Smart City (SmartCity): 2016
- IEEE Reconfigurable Architectures Workshop (RAW): 2014, and 2015
- IEEE International Conference on Cyber, Physical and Social Computing (CPSCoM): 2015
- International Symposium on Applied Reconfigurable Computing (ARC): 2014
- HiPEAC Workshop on Reconfigurable Computing (WRC): 2014
- IEEE International Conference on Networking, Architecture, and Storage (NAS): 2013
- IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC): 2012

### **2.0.5 Program Vice-Chair**

- IEEE Reconfigurable Architectures Workshop (RAW): 2013
- IEEE/IFIP International Conference on Embedded and Ubiquitous Computing (EUC) - Embedded Systems and Hardware/Software Co-Design: 2011

### **2.0.6 Topic Chair**

- Topic Chair for the topic A6: Reliable and Reconfigurable Systems in Design, Automation & Test in Europe (DATE): 2015

### **2.0.7 Topic Co-Chair**

- Topic Co-Chair for the topic A6: Reliable and Reconfigurable Systems in Design, Automation & Test in Europe (DATE): 2012, 2013, and 2014

### **2.0.8 Track Chair**

- IEEE International Conference on Field Programmable Logic and Applications (FPL): 2013

### **2.0.9 Workshop Organizer**

- IEEE Reconfigurable Architectures Workshop (RAW): 2016, 2017
- International Workshop on Computing in Heterogeneous, Autonomous 'N' Goal-oriented Environments (CHANGE): 2011, 2012, 2013, 2014, 2015, and 2016

### 2.0.10 Program Committee

- International Conference on Computer-Aided Design (ICCAD): 2015, 2016, and 2017
- IEEE International Symposium on Field-Programmable Custom Computing Machines (FCCM): 2013, 2014, 2015, 2016, and 2017
- Conference on Hardware/Software CoDesign and System Synthesis (CODES-ISSS): 2011, 2012, 2013, 2014, 2015, 2016, and 2017
- IEEE International Conference on Field Programmable Logic and Applications (FPL): 2008, 2009, 2010, 2011, 2012, 2013, 2014, 2015, 2016, and 2017
- IEEE Reconfigurable Architectures Workshop (RAW): 2007, 2008, 2009, 2010, 2011, 2012, 2013, 2014, 2015, 2016, and 2017
- International Conference on Embedded Computer Systems: Architectures, Modeling and Simulation (SAMOS): 2015, 2016, and 2017
- ACM International Conference on Computing Frontiers (CF): 2016, and 2017
- IEEE International Conference on Cloud and Autonomic Computing (ICCAC): 2015, 2016, and 2017
- IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA): 2015, 2016, and 2017
- NASA/ESA Conference on Adaptive Hardware and Systems (AHS): 2012, 2013, 2014, 2015 and 2017
- International Symposium on Applied Reconfigurable Computing (ARC): 2015, 2016, and 2017
- IEEE International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MC-SoC): 2015, and 2017
- Design, Automation & Test in Europe (DATE): 2012, 2013, 2014, 2015, and 2016
- International Conference on ReConFIGurable Computing and FPGAs (ReConFig): 2006, 2008, 2009, 2010, 2011, 2012, 2013, 2014, 2015, and 2016
- International Workshop on Heterogeneous High-performance Reconfigurable Computing (H2RC): 2016
- International Workshop on Reconfigurable Communication-centric Systems-on-Chip (Re-CoSoC): 2012, 2013, 2014, 2015, and 2016
- Workshop on Reconfigurable Computing (WRC): 2010, 2011, 2012, 2013, 2014, 2015, and 2016
- International Workshop on Highly Efficient Accelerators and Reconfigurable Technologies (HEART): 2010, and 2016
- IEEE/IFIP International Conference on Embedded and Ubiquitous Computing: 2009, 2010, 2011, 2012, 2013, 2014, and 2015
- Design Automation Conference (DAC): 2012, 2013, and 2014
- Embedded Operating System Workshop (EWiLi): 2013, 2014, and 2015
- IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC): 2014
- International Workshop on Adaptive and Reconfigurable Embedded Systems (APRES): 2014
- International Workshop on Reconfigurable Architectures and Self-adaptive Autonomic Systems (RASAS): 2014
- International Workshop on Multi-Objective Many-Core Design (MOMAC): 2014

- IEEE International Conference on Emerging Technologies & Factory Automation (ETFAs): 2014
- Workshop on Virtual Prototyping of Parallel and Embedded Systems (ViPES): 2014
- IEEE International Conference on Networking, Architecture, and Storage (NAS): 2010, 2011, 2012, and 2013
- International Symposium on System-on-Chip (SoC): 2009, 2010, 2011, 2012, and 2013
- Southern Conference on Programmable Logic (SPL) Conference: 2008, 2009, 2010, 2011, and 2012
- International Conference on Engineering of Reconfigurable Systems and Algorithms (ERSA) Conference: 2006, 2007, 2008, 2009, 2010, 2011, and 2012
- International Workshop on Mobile Computing in the Clouds (MobiCloud): 2013
- IEEE International Symposium on Embedded Multicore SoCs (MCSoc): 2013
- IEEE Real-Time and Embedded Technology and Applications Symposium, RTAS (Applied Methodologies and Foundations): 2012
- European Conference on the Applications of Evolutionary Computation (EVO): 2011
- IEEE Field Programmable Technology (FPT): 2007, 2008, 2009, and 2010
- SPIE Conference: Microtechnologies for the New Millennium 2009 and 2010
- IEEE Computer Society Annual Symposium on VLSI (ISVLSI): 2009 and 2010
- International Workshop on Reconfigurable and Multicore Embedded Systems (WoRMES): 2009

## 2.1 Program committee, conference organization and revision activities

### 2.1.1 International Workshops Organization

- **International coordinator**

International Conference on Industrial and Information System, ICIIS 07 Date: August, 2007

Location: University of Peradeniya, Sri Lanka

Conference Chairman: Janaka Ekanayake

Workshop Chairman: Marco D. Santambrogio

Workshop Title: Reconfigurable Computing

### 2.1.2 National Workshops and Seminars Organization

- Scientific board member *CALL4BRAIN* - <http://www.call4brain.com/>, TEDMED, De Donato - Politecnico di Milano, Milano, Dec. 2, 2016.
- *Reconfigurable Computing Italian Workshop*, first Italian Meeting, S01 - Politecnico di Milano, Milano, Dec. 19, 2008.
- *Partial Dynamic Reconfiguration Workshop*, Nokia Siemens Networks, Cinisello Balsamo, Milano, April 23, 2008.
- *IEEE Expert now: Tutorial on-line*, IEEE Delegation, DEI - Politecnico di Milano, September 2007.



### 2.1.3 Revision activities

- IEEE Transactions on Very Large Scale Integration Systems (TVLSI)
- IEEE Transaction on Computer-Aided Design (TCAD)
- IEEE Transaction on Computer (TOC)
- IEEE Micro
- IEEE Transactions on Industrial Informatics (TII)
- IEEE Transaction on Evolutionary Computation
- IEEE Embedded Systems Letters (ESL)
- IEEE Transactions on Signal Processing
- ACM Computing Reviews
- ACM Transaction on Reconfigurable Technology and Systems (TRETS)
- ACM Transactions on Embedded Computing Systems (TECS)
- Design Automation for Embedded Systems (DAEM)
- Journal of Systems Architecture (JSA) - Elsevier
- Integration, the VLSI Journal - Elsevier
- Computers & Electrical Engineering - Elsevier
- International Journal of Circuit Theory and Applications (IJCTA) - Wiley
- Journal of Computer Science and Technology (JCST)
- International Conferences:
  - Design Automation Conference (DAC);
  - Design, Automation and Test in Europe (DATE);
  - IEEE International Symposium on Circuits and Systems (ISCAS);
  - IEEE International Conference on Field Programmable Logic and Applications (FPL);
  - IEEE Reconfigurable Architectures Workshop (RAW);
  - IEEE Computer Society Annual Symposium on VLSI (ISVLSI);
  - IEEE International Conference on Field-Programmable Technology (FPT);
  - ACM Great Lakes Symposium on VLSI (GLSVLSI);
  - IEEE/IFIP International Conference on Embedded and Ubiquitous Computing;
  - International Workshop on Reconfigurable and Multicore Embedded Systems (WoRMES);
  - IFIP International Conference on Very Large Scale Integration (VLSI-SoC);
  - International Symposium on System-on-Chip (ISSoC);
  - Southern Conference on Programmable Logic (SPL) Conference;
  - Engineering of Reconfigurable Systems and Algorithms (ERSA);
  - International Conference on Green Circuits and Systems (ICGCS).

## 2.2 Invited talks and Panelists

- Invited talks

- *Heterogeneous exascale Supercomputing: the Role of CAD in the exaFPGA Project.*  
Speaker: M. D. Santambrogio.  
Design, Automation & Test in Europa, Talk in a special session at DATE 17, Lausanne, Switzerland, 28 March, 2017
- *NECST and Xilinx: an interesting history.*  
Speaker: M. D. Santambrogio.  
Xilinx, San Jose, CA, USA, 2 June, 2016
- *The NECST experience.*  
Speaker: M. D. Santambrogio.  
Oracle Labs, Redwood Shores, CA, USA, 1 June, 2016
- *A self-adaptive approach to efficiently manage energy and performance in tomorrow's heterogeneous computing systems.*  
Speaker: M. D. Santambrogio.  
Design, Automation & Test in Europa, Talk in an special session at DATE 16, Dresden, Germany, 16 March, 2016
- *Operating System-Level Performance and Power Management: from Datacenters to Embedded Systems.*  
Speaker: M. D. Santambrogio.  
The International Conference on Hardware-Software Codesign and System Synthesis, CODES+ISSS 15, Amsterdam, Dutch, 6 October, 2015
- *A bird's eye view on the NECST Lab: What (i/wa)s the DRES D project.*  
Speaker: M. D. Santambrogio.  
Xilinx, San Jose, CA, USA, 10 June, 2015
- *On how to implement efficient mobile devices power modelling, and design methodologies for smart spaces.*  
Speaker: M. D. Santambrogio.  
Samsung Research America, Mountain View, CA, USA, 9 June, 2015
- *From FPGA-based Reconfigurable Systems to Autonomic Adaptive Computing Systems an Enabling Technologies Perspective.*  
Speaker: M. D. Santambrogio  
Stanford University, Stanford, CA, USA, 6 June, 2014
- *Cores Allocation and Relocation Management for Self Dynamic Reconfigurable Systems.*  
Speaker: M. D. Santambrogio  
ALTERA, San Jose, CA, USA, 5 June, 2014
- *A bird's eye view on the NECST Lab and behind the scene of this meeting.*  
Speaker: M. D. Santambrogio  
Berkeley, CA, USA, 2 June, 2014
- *How to enable FPGA-based self-adaptive computing systems - Keynote Speech*  
Speaker: M. D. Santambrogio  
Workshop co-located with FPL 2013 entitled "Challenges of Embedded Robotics. Can FPGAs Overcome Them?", Porto, Portugal, 6 September, 2013.
- *A bird's eye view on the NECST Lab and behind the scene of this meeting.*  
Speaker: M. D. Santambrogio.  
MIT, MA, USA, 22 May, 2013
- *The Autonomic Operating System Research Project - Achievements and Future Directions.*  
Speaker: M. D. Santambrogio.  
Design Automation Conference, DAC 2013, Austin, Texas, June 2 - 6, 2013
- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Invited Speaker: M. D. Santambrogio.  
Workshop on Self-Awareness in Reconfigurable Computing Systems (SRCS), Oslo, Norway, September 1, 2012

- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Speaker: M. D. Santambrogio and F. Sironi.  
IBM Research, Austin, TX, USA, 9 August, 2012
- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Speaker: M. D. Santambrogio and F. Sironi.  
Oracle, Belmont, CA, USA, 8 June, 2012
- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Speaker: M. D. Santambrogio and F. Sironi.  
Yahoo, Santa Clara, CA, USA, 4 June, 2012
- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Speaker: M. D. Santambrogio.  
University of Illinois at Chicago, Chicago, IL, USA, September 15, 2011
- *The CHANGE project: Enabling Technologies For Self-Aware Adaptive Computing Systems.*  
Speaker: M. D. Santambrogio.  
GDR SOC-SIP, Systemes multiprocesseurs : outil, modele pour l'estimation de la consommation, techniques d'optimisation, Paris, France, March 21, 2011
- *Enabling Technologies For Self-Aware Adaptive Systems.*  
M. D. Santambrogio, H. Hoffmann, J. Eastep, J. E. Miller, A. Agarwal.  
NASA/ESA Conference on Adaptive Hardware and Systems, AHS 2010, Anaheim California, USA, June 15 - 18, 2010
- *From Reconfigurable Architectures to Self-Adaptive Autonomic Systems.* - Keynote Speech  
Speaker: M. D. Santambrogio  
International Workshop on Reconfigurable and Multicore Embedded System, August 29, 2009.
- *A parallel-serial decimal multiplier architecture.*  
L. Dadda, M. D. Santambrogio  
4th Logic Synthesis Italian Day, Politecnico di Milano, Milano, June 30, 2008.
- *Reconfiguration technologies and DRESD contribution.*  
Speaker: M. D. Santambrogio  
Nokia Siemens Networks, Cinisello Balsamo, Milano, April 23, 2008.
- *Design Flow for System-on-Programmable Chip.*  
Speaker: M. D. Santambrogio  
Nokia Siemens Networks, Cinisello Balsamo, Milano, April 23, 2008.
- *Partial Dynamic Reconfiguration: Real Needs and Limits.*  
Speaker: M. D. Santambrogio  
Nokia Siemens Networks, Cinisello Balsamo, Milano, April 23, 2008.
- *Partial Dynamic Reconfiguration: Basic Concepts.*  
Speaker: M. D. Santambrogio  
Nokia Siemens Networks, Cinisello Balsamo, Milano, April 23, 2008.
- *Dynamic Reconfigurability in Embedded System Design.*  
Speaker: M. D. Santambrogio  
Heinz Nixdorf Institute, Paderborn, Germany, January 26, 2006.

- **Panelist**

- April 24 - 15, 2012, Goteborg, Sweden, HiPEAC Computing Systems Week April 2012, *Design and runtime management of reconfigurable systems*
- July 9 - 11, 2012, York, UK, 7th International Workshop on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC), *Maturity of high level synthesis tools - useful ore just another hype?*

## 3 TEACHING ACTIVITIES

### 3.1 Courses

- University: Politecnico di Milano (Milano, Italy):
  - **PhD Course**
    - \* Course: Advanced Topic on Reconfigurable FPGA-Based Systems Design  
Teacher: prof. M. D. Santambrogio, prof. A. R. Miele  
Academic Year: 16/17
    - \* Course: Advanced Topics on Heterogeneous System Architectures: architectures, programming models and resource management  
Teacher: prof. M. D. Santambrogio, prof. A. R. Miele  
Academic Year: 15/16
    - \* Course: FPGA-Based Systems Design  
Teacher: prof. M. D. Santambrogio  
Academic Year: 13/14
    - \* Course: Parallelism in Wonderland; are you ready to see how deep the rabbit hole goes?  
Teachers: prof. M. D. Santambrogio, prof. S. Campanoni  
Academic Year: 13/14
    - \* Course: From Reconfigurable Hardware to Self-Adaptive Computer Architectures  
Teacher: prof. M. D. Santambrogio  
Academic Year: 13/14, 12/13
  - **Undergraduate Course**
    - \* Course: Informatica ed Elementi di Informatica Medica - (Bio Eng students)  
Teacher: prof. M. D. Santambrogio  
Academic Year: 16/17, 15/16, 14/15, 13/14, 12/13, 11/12
    - \* Course: Informatica B - (Mech Eng studentes)  
Teacher: prof. M. D. Santambrogio  
Academic Year: 16/17, 15/16, 14/15, 13/14, 10/11
    - \* Course: Projects of Ingegneria Informatica - Undergraduate Course  
Teacher: prof. M. D. Santambrogio  
Academic Year: 09/10, 08/09
- University: University of Illinois at Chicago (Chicago, Illinois - USA):
  - Course: Advanced Computer Architecture (CS/ECE/MENG 466) - Graduate Course (taught in English)  
Teacher: prof. M. D. Santambrogio  
Academic Year: 13/14, 11/12, 10/11, 09/10

### 3.2 Teaching Assistantships

- University: Politecnico di Milano (Milano, Italy):
  - Course: Architettura Avanzata dei Calcolatori (Advanced Computer Architecture) - Graduate Course  
Teacher: prof. D. Sciuto  
Academic Year: 16/17, 15/16, 14/15, 10/11
  - Course: High Performance and Processors and Systems - Graduate course (taught in English)  
Teacher: prof. D. Sciuto  
Academic Year: 13/14, 12/13, 11/12, 10/11, 09/10, 07/08, 06/07
  - Course: Architettura Avanzata dei Calcolatori (Advanced Computer Architecture) - Graduate Course  
Teacher: prof. R. Negrini  
Academic Year: 12/13, 11/12

- Course: Architettura dei Calcolatori (Computer Architecture) - Graduate Course  
Teacher: prof. D. Sciuto  
Academic Year: 08/09, 07/08, 06/07
- Course: Reti Logiche A - Undergraduate Course  
Teacher: prof. C. Bolchini  
Academic Year: 08/09, 07/08, 06/07, 05/06, 04/05
- Course: Reti Logiche B - Undergraduate Course  
Teacher: prof. F. Salice  
Academic Year: 05/06
- Course: Informatica I - Undergraduate Course  
Teacher: prof. C. Bolchini  
Academic Year: 05/06
- University: Advanced Learning and Research Institute, master in Embedded Systems (Lugano, Switzerland):
  - Course: Design Technologies - Graduate course (taught in English)  
Teacher: prof. G. De Micheli  
Academic Year: 07/08, 06/07
  - Course: Validation and Verification - Graduate course (taught in English)  
Teacher: prof. F. Somenzi  
Academic Year: 06/07, 05/06, 04/05
- University: Universtia' degli Studi di Milano (Crema, Italy)
  - Course: Sistemi Operativi (Operating System) - Undergraduate Course  
Teacher: prof. V. Piuri  
Academic Year: 08/09, 07/08, 06/07, 05/06

### 3.3 Theses Supervision

- **Advisor of the following PhD Theses at Politecnico di Milano:**
  - *Enabling power-awareness for multi-tenant systems.*  
Student: M. Ferroni. Politecnico di Milano, 22 Feb 2017;
  - *On the design of autonomic techniques for runtime resource management in heterogeneous systems.*  
Student: G. C. Durelli. Politecnico di Milano, 5 Feb 2016;
  - *On the role of polyhedral analysis in high performance reconfigurable hardware based computing systems.*  
Student: R. Cattaneo. Politecnico di Milano, 17 Dec 2015;
  - *Techniques and Tools for Efficient, Qos-Driven Warehouse-Scale Computing.*  
Student: D. B. Bartolini. Politecnico di Milano, 16 Feb 2015;
  - *System Support for Adaptive Performance and Thermal Management of Chip-Multiprocessors.*  
Student: F. Sironi. Politecnico di Milano, 21 March 2014;
- **Advisor of the following Master Theses in CS at UIC, from 2008: 28**
- **Advisor of Master Theses in Computer Engineering at Politecnico di Milano, from 2009: 48**
- **Advisor of Bachelor Theses in Biomedical Engineering at Politecnico di Milano, from 2013: 4**
- **Advisor of Bachelor Theses in CS at Politecnico di Milano, during the years 2008 - 2014: 34**
- **Co-Advisor of the following Master Theses:**  
From 2005 until today, Marco D. Santambrogio has been the co-advisor of more than 20 CS students for their M.Sc. thesis works at Politecnico di Milano

- **Co-Advisor of the following Bachelor Theses:**

From 2005 until today, Marco D. Santambrogio has been the co-advisor of more than 80 CS students for their bachelor thesis works at Politecnico di Milano

## 4 AWARDS AND RESEARCH GRANTS

### 4.1 Awards

- Advisor of the PhD and Student FPGA category winners of the **Xilinx Open Hardware Contest 2016**. (PhD FPGA Category winner *"exaFPGA: Iterative stencil cell loop acceleration"*, Student FPGA Category winner *ProFAX: Protein folding on FPGA* [[www.openhw.eu/2016-finalists](http://www.openhw.eu/2016-finalists)])
- Co-author of the paper winner of the **RAW 2016 Best Demo Award**. (*"On the Automation of High Level Synthesis of Convolutional Neural Networks"*. [[10]])
- Co-author of the paper winner of the **EWiLi 2013 Best Paper Award**. (*"A Performance-Aware Quality of Service-Driven Scheduler for Multicore Processors"*. [[9]])
- Co-author of the paper winner of the **2012 HiPEAC Paper Award**. (*"Metronome: Operating System Level Performance Management via Self-Adaptive Computing"*. [[61]])
- Co-author of the **Best Student Paper award sponsored by IEEE TCPP**: 7th IEEE International Conference on Autonomic Computing (ICAC) 2010 (*Smartlocks: Lock Acquisition Scheduling for Self-Aware Synchronization* [[68]])
- Co-author of the Best Paper Award Finalist, IEEE International Conference on Field Programmable Logic and Applications (FPL) 2009 (*A Runtime Relocation Based Workflow for Self Dynamic Reconfigurable Systems Design* [[84]])
- December, 2008. awarded a **Progetto Rocca Postdoc Fellowship at MIT**.
- **Dimitri N. Chorafas PhD Thesis Award** from the Chorafas Foundation (Berne, Switzerland) for the best PhD Theses in "Systems Engineering and Information Technology", May 2008. Thesis title: *Hardware/Software codesign methodologies for dynamically reconfigurable systems*)
- Co author of the **Best paper award**: 15th International Conference on Very Large Scale Integration, IFIP VLSI-SoC 2007 (*ReCPU: a Parallel and Pipelined Architecture for Regular Expression Matching* [[108]])

### 4.2 Research Grants:

- **H2020 EXTRA European project**.  
PoliMi PI: M. D. Santambrogio  
Period: Sept 2015 - Sept 2018  
Funding: 450K EUR  
Santambrogio's role in EXTRA:
  - PoliMi PI for EXTRA
  - General Assembly Member of the EXTRA project
  - Work package Leader of WP4 (*Development of Reconfigurable Tools Platform*)
- **FP7 SAVE European project**.  
PoliMi PI: Prof. C. Bolchini ([cristiana.bolchini@polimi.it](mailto:cristiana.bolchini@polimi.it))  
Period: Sept 2013 - Sept 2016  
Santambrogio's role in SAVE:
  - Task Leader of the following tasks: T3.1 (*Adaptive host OS components*) and T3.2 (*Adaptation policies/strategies*)
- **FP7 FASTER European project**.  
PoliMi PI: Prof. D. Sciuto ([donatella.sciuto@polimi.it](mailto:donatella.sciuto@polimi.it))  
Period: Sept 2011 - Dec 2014  
Santambrogio's role in EXTRA:
  - Work package Leader of WP2 (*High-level analysis and reconfigurable system definition*)

- Task Leader of the following tasks: T2.2 (*Application task profiling and identification of reconfigurable cores*) and T2.4 (*Compile-time baseline scheduling and core mapping onto reconfigurable regions*)

- **Fondazione Cariplo - Regione Lombardia**

Project Title: A Challenges Driven Design for Effective and Efficient Autonomic Mobile Computing Architectures.

Period: Oct 2016 - March 2018

Funding: 100K EUR

Santambrogio's role in the project: Principal Investigator

- **Humanitas**

Project Title: HUNA: Humanitas University NECST Application

Period: Dec 2016 - Jun 2018

Funding: 75K EUR

Santambrogio's role in the project: Principal Investigator

- **MathWorks Academic Support**

Project Title: Teaching computer programming and embedded systems with audio processing in MATLAB.

Period: Sept 2016 - Jun 2017

Funding: 38K EUR

Santambrogio's role in the project: Principal Investigator

- **Progetto Rocca Workshops and Colloquia.**

Collaboration between the MIT-CARBON Group and Politecnico di Milano - DRESD Group.

Period: January 2010

Funding: 25K USD

- **Swiss NSF Research Project (Division II).**

Project title: *Dynamically Adaptive Architectures for Nomadic Embedded Systems.*

Period: 02/2010 - 01/2013.

Partners: EPFL, Prof. David Atienza, in cooperation with Politecnico di Milano (IT): Prof. Donatella Sciuto and Dr. Marco D. Santambrogio.

- **HiPEAC Collaboration Grant.**

Title of the research: Self-Aware Reconfigurable Computing Systems for Energy Saving and Performance Enhancement.

Period: November 2010 - December 2011

Funding: 5K EUR

- **HiPEAC Collaboration Grant.**

Title of the research: Self-aware and autonomic system.

Period: July 2009

Funding: 5K EUR



## 5 PUBLICATIONS

### Book

- [1] Pao-Ann Hsiung, M. D. SANTAMBROGIO, and Chun-Hsian Huang. *Reconfigurable System Design and Verification*. Taylor & Francis/CRC Press, 2009.

### Journals

- [1] M. Rabozzi, G. C. Durelli, A. Miele, J. Lillis, and MARCO D. SANTAMBROGIO. “Floor-planning Automation for Partial-Reconfigurable FPGAs via Feasible Placements Generation”. In: *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)* 25.1 (2017), pp. 151–164. ISSN: 1063-8210. DOI: 10.1109/TVLSI.2016.2562361.
- [2] Riccardo Cattaneo, Giuseppe Natale, Carlo Sicignano, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “On How to Accelerate Iterative Stencil Loops: A Scalable Streaming-Based Approach”. In: *ACM Transactions on Architecture and Code Optimization (TACO)* 12.4 (2016), p. 53. DOI: 10.1145/2842615. URL: <http://doi.acm.org/10.1145/2842615>.
- [3] Gianluca C. Durelli and MARCO D. SANTAMBROGIO. “Autonomic thread scaling library for QoS management”. In: *SIGBED Review* 13.1 (2016), pp. 41–47. DOI: 10.1145/2907972.2907978. URL: <http://doi.acm.org/10.1145/2907972.2907978>.
- [4] Alberto Scolari, Davide Basilio Bartolini, and MARCO D. SANTAMBROGIO Santambrogio. “A Software Cache Partitioning System for Hash-Based Caches”. In: *ACM Transactions on Architecture and Code Optimization (TACO)* 13.4 (Dec. 2016), 57:1–57:24. ISSN: 1544-3566. DOI: 10.1145/3018113. URL: <http://doi.acm.org/10.1145/3018113>.
- [5] Dionisios N. Pnevmatikatos, Kyprianos Papadimitriou, Tobias Becker, Peter Böhm, Andreas Brokalakis, Karel Bruneel, C. Ciobanu, Tom Davidson, Georgi Gaydadjiev, Karel Heyse, Wayne Luk, Xinyu Niu, Ioannis Papaefstathiou, Danilo Pau, Oliver Pell, Christian Pilato, MARCO D. SANTAMBROGIO, Donatella Sciuto, Dirk Stroobandt, Tod Todman, and Elias Vansteenkiste. “FASTER: Facilitating Analysis and Synthesis Technologies for Effective Reconfiguration”. In: *Microprocessors and Microsystems - Embedded Hardware Design* 39.4-5 (2015), pp. 321–338. DOI: 10.1016/j.micpro.2014.09.006. URL: <http://dx.doi.org/10.1016/j.micpro.2014.09.006>.
- [6] Davide B. Bartolini, Filippo Sironi, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “Automated Fine-Grained CPU Provisioning for Virtual Machines”. In: *ACM Transactions on Architecture and Code Optimization (TACO)* 11.3 (2014), 27:1–27:25. DOI: 10.1145/2637480. URL: <http://doi.acm.org/10.1145/2637480>.
- [7] Xabier Iturbe, Ali Ebrahim, Khaled Benkrid, Chuan Hong, Tughrul Arslan, Jon Perez, Didier Keymeulen, and MARCO D. SANTAMBROGIO. “R3TOS-Based Autonomous Fault-Tolerant Systems”. In: *IEEE Micro* 34.6 (2014), pp. 20–30. DOI: 10.1109/MM.2014.58. URL: <http://dx.doi.org/10.1109/MM.2014.58>.
- [8] Jacopo Panerati, Martina Maggio, Matteo Carminati, Filippo Sironi, Marco Triverio, and MARCO D. SANTAMBROGIO. “Coordination of Independent Loops in Self-Adaptive Systems”. In: *ACM Transaction on Reconfigurable Technology and Systems (TRETs)* 7.2 (2014), 12:1–12:16. DOI: 10.1145/2611563. URL: <http://doi.acm.org/10.1145/2611563>.
- [9] Filippo Sironi, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “A performance-aware quality of service-driven scheduler for multicore processors”. In: *SIGBED Review* 11.1 (2014), pp. 50–55. DOI: 10.1145/2597457.2597464. URL: <http://doi.acm.org/10.1145/2597457.2597464>.
- [10] Ravi Jhawar, Vincenzo Piuri, and MARCO D. SANTAMBROGIO. “Fault Tolerance Management in Cloud Computing: A System-Level Perspective”. In: *IEEE Systems Journal* 7.2 (2013), pp. 288–297. DOI: 10.1109/JSYST.2012.2221934. URL: <http://dx.doi.org/10.1109/JSYST.2012.2221934>.
- [11] Martina Maggio, Henry Hoffmann, MARCO D. SANTAMBROGIO, Anant Agarwal, and Alberto Leva. “Power Optimization in Embedded Systems via Feedback Control of Resource Allocation”. In: *IEEE Trans. Contr. Sys. Techn.* 21.1 (2013), pp. 239–246. DOI: 10.1109/TCST.2011.2177499. URL: <http://dx.doi.org/10.1109/TCST.2011.2177499>.

- [12] A. A. Nacci, Francesco Trovò, F. Maggi, Matteo Ferroni, Andrea Cazzola, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “Adaptive and Flexible Smartphone Power Modeling”. In: *MONET* 18.5 (2013), pp. 600–609. DOI: 10.1007/s11036-013-0470-y. URL: <http://dx.doi.org/10.1007/s11036-013-0470-y>.
- [13] Vincenzo Rana, Alessandro Antonio Nacci, Ivan Beretta, MARCO D. SANTAMBROGIO, David Atienza, and Donatella Sciuto. “Design Methods for Parallel Hardware Implementation of Multimedia Iterative Algorithms”. In: *IEEE Design & Test* 30.4 (2013), pp. 71–80. DOI: 10.1109/MDT.2012.2223191. URL: <http://dx.doi.org/10.1109/MDT.2012.2223191>.
- [14] Fabio Cancare, Davide B. Bartolini, Matteo Carminati, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “On the Evolution of Hardware Circuits via Reconfigurable Architectures”. In: *ACM Transaction on Reconfigurable Technology and Systems (TRET)* 5.4 (2012), p. 22. DOI: 10.1145/2392616.2392620. URL: <http://doi.acm.org/10.1145/2392616.2392620>.
- [15] Martina Maggio, Henry Hoffmann, Alessandro Vittorio Papadopoulos, Jacopo Panerati, MARCO D. SANTAMBROGIO, Anant Agarwal, and Alberto Leva. “Comparison of Decision-Making Strategies for Self-Optimization in Autonomic Computing Systems”. In: *ACM Transactions on Autonomous and Adaptive Systems (TAAS)* 7.4 (2012), p. 36. DOI: 10.1145/2382570.2382572. URL: <http://doi.acm.org/10.1145/2382570.2382572>.
- [16] Marco Maggioni, MARCO D. SANTAMBROGIO, and Jie Liang. “GPU-accelerated Chemical Similarity Assessment for Large Scale Databases”. In: *Proceedings of the International Conference on Computational Science, ICCS 2011, Nanyang Technological University, Singapore, 1-3 June, 2011*. Ed. by Mitsuhsa Sato, Satoshi Matsuoka, Peter M. A. Sloot, G. Dick van Albada, and Jack Dongarra. Vol. 4. Procedia Computer Science. Elsevier, 2011, pp. 2007–2016. DOI: 10.1016/j.procs.2011.04.219. URL: <http://dx.doi.org/10.1016/j.procs.2011.04.219>.
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- [20] Alessio Montone, MARCO D. SANTAMBROGIO, Donatella Sciuto, and Seda Oğrenci Memik. “Placement and Floorplanning in Dynamically Reconfigurable FPGAs”. In: *ACM Transaction on Reconfigurable Technology and Systems (TRET)* 3.4 (2010), p. 24. DOI: 10.1145/1862648.1862654. URL: <http://doi.acm.org/10.1145/1862648.1862654>.
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- [22] Simone Corbetta, Massimo Morandi, Marco Novati, MARCO D. SANTAMBROGIO, Donatella Sciuto, and Paola Spoletini. “Internal and External Bitstream Relocation for Partial Dynamic Reconfiguration”. In: *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)* 17.11 (2009), pp. 1650–1654. DOI: 10.1109/TVLSI.2008.2005670. URL: <http://dx.doi.org/10.1109/TVLSI.2008.2005670>.
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- [24] Francesco Redaelli, MARCO D. SANTAMBROGIO, and Seda Ogrenci Memik. “An ILP Formulation for the Task Graph Scheduling Problem Tailored to Bi-Dimensional Reconfigurable Architectures”. In: *Int. J. Reconfig. Comp.* 2009 (2009), 541067:1–541067:12. DOI: 10.1155/2009/541067. URL: <http://dx.doi.org/10.1155/2009/541067>.

## Editorials

- [1] Houcine Hassan, Laurence T. Yang, Haibo Zhang, and MARCO D. SANTAMBROGIO. “Special Issue on: Multicore and Many-core Architectures for Future Generation Embedded Systems”. In: *Future Generation Comp. Syst.* 56 (2016), pp. 169–170. DOI: 10.1016/j.future.2015.11.017. URL: <http://dx.doi.org/10.1016/j.future.2015.11.017>.
- [2] MARCO D. SANTAMBROGIO and Ramachandran Vaidyanathan. “Guest Editorial RAW 2014”. In: *ACM Transaction on Reconfigurable Technology and Systems (TRETS)* 9.2 (2016), p. 13. DOI: 10.1145/2841314. URL: <http://doi.acm.org/10.1145/2841314>.
- [3] Diana Goehringer, MARCO D. SANTAMBROGIO, João M. P. Cardoso, and Koen Bertels. “Guest Editorial ARC 2014”. In: *ACM Transaction on Reconfigurable Technology and Systems (TRETS)* 9.1 (2015), p. 5. DOI: 10.1145/2831431. URL: <http://doi.acm.org/10.1145/2831431>.
- [4] J. Morris Chang, MARCO D. SANTAMBROGIO, and Pao-Ann Hsiung. “Embedded multicore systems: Architecture, performance and application”. In: *Microprocessors and Microsystems - Embedded Hardware Design* 37.8-B (2013), pp. 885–886. DOI: 10.1016/j.micpro.2013.11.001. URL: <http://dx.doi.org/10.1016/j.micpro.2013.11.001>.
- [5] Claudia Feregrino, Miguel Arias, Kris Gaj, Viktor K. Prasanna, MARCO D. SANTAMBROGIO, and Ron Sass. “Selected Papers from the International Conference on Reconfigurable Computing and FPGAs (ReConFig’10)”. In: *Int. J. Reconfig. Comp.* 2012 (2012), 319827:1–319827:2. DOI: 10.1155/2012/319827. URL: <http://dx.doi.org/10.1155/2012/319827>.
- [6] Ignacio Bravo Muñoz and MARCO D. SANTAMBROGIO. “Design flows and system architectures for adaptive computing on reconfigurable platforms”. In: *Journal of Systems Architecture - Embedded Systems Design* 56.11 (2010), pp. 543–544. DOI: 10.1016/j.sysarc.2010.10.007. URL: <http://dx.doi.org/10.1016/j.sysarc.2010.10.007>.
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## Conferences

- [1] P. Cancian, G. W. Di Donato, V. Rana, and MARCO D. SANTAMBROGIO. “An embedded Gabor-based palm vein recognition system”. In: *2017 IEEE EMBS International Conference on Biomedical Health Informatics (BHI)*. 2017, pp. 405–408. DOI: 10.1109/BHI.2017.7897291.
- [2] L. Cerina and M. D. Santambrogio. “Reconfigurable embedded systems applications for versatile biomedical measurements”. In: *Design, Automation Test in Europe Conference Exhibition (DATE), 2017*. 2017, pp. 1420–1425. DOI: 10.23919/DATE.2017.7927215.
- [3] A. Purgato, MARCO D. SANTAMBROGIO, T. Berger-Wolf, and A. G. Forbes. “Interactive visualization for brain spatio-temporal networks”. In: *2017 IEEE EMBS International Conference on Biomedical Health Informatics (BHI)*. 2017, pp. 21–24. DOI: 10.1109/BHI.2017.7897195.
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- [5] D. Stroobandt, C. B. Ciobanu, M. D. Santambrogio, G. Figueiredo, A. Brokalakis, D. Pnevmatikatos, M. Huebner, T. Becker, and A. J. W. Thom. “An open reconfigurable research platform as stepping stone to exascale high-performance computing”. In: *Design, Automation Test in Europe Conference Exhibition (DATE), 2017*. 2017, pp. 416–421. DOI: 10.23919/DATE.2017.7927026.
- [6] L. Di Tucci, K. O’Brien, M. Blott, and M. D. Santambrogio. “Architectural optimizations for high performance and energy efficient Smith-Waterman implementation on FPGAs using OpenCL”. In: *Design, Automation Test in Europe Conference Exhibition (DATE), 2017*. 2017, pp. 716–721. DOI: 10.23919/DATE.2017.7927082.
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- [8] Giuseppe Natale, Giulio Stramondo, Pietro Bressana, Riccardo Cattaneo, Donatella Sciuto, and MARCO D. SANTAMBROGIO. “A polyhedral model-based framework for dataflow implementation on FPGA devices of iterative stencil loops”. In: *Proceedings of the 35th International Conference on Computer-Aided Design, ICCAD 2016, Austin, TX, USA, November 7-10, 2016*. Ed. by Frank Liu. ACM, 2016, p. 77. DOI: 10.1145/2966986.2966995. URL: <http://doi.acm.org/10.1145/2966986.2966995>.
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